



Director of RF IC Design

Energous Corporation (Nasdaq: WATT) is the Wireless Power Network global leader. Our award-winning WattUp® wireless charging solution is the only technology that supports both contact and distance charging through a fully compatible ecosystem. Built atop fast, efficient, and highly scalable RF-based charging technology. Energous develops silicon-based wireless power transfer (WPT) technologies and customizable reference designs, and provides worldwide regulatory assistance, a reliable supply chain, quality assurance, and sales and technical support to global customers. The company received the world's first FCC Part 18 certification for at-a-distance wireless charging and has been awarded over 200 patents for its WattUp wireless charging technology to-date.

Our goal is simple: To power everything from the critical tools and devices that keep factories running to the instruments and wearables that monitor patient health – wirelessly. Our next-generation technology – built atop innovative engineering and backed by hundreds of patents – supports a near-limitless range of applications without the need for cumbersome charging cables and ports that limit innovation and are prone to failure. WattUp delivers advanced capabilities and design flexibility to global manufacturers who are building the latest consumer, medical, military, and industrial devices, among many other sectors.

Responsibilities include, but are not limited to, the following:

- Interview and hire top talent for the RFIC team
- Lead and provide technical direction for RFIC team
- Design and layout of RF, analog, and digital blocks in different COMS process nodes
- Own and maintain all simulation design licenses for the IC team
- IP and SOC integration into an RFIC chip
- Work with marketing and other teams to establish POR MRD, PRD, system architecture and specifications
- Study competitive products and technologies, and perform analysis and benchmarking on competitor devices
- Qualify design opportunities and establish value proposition relative to the competition

Competencies:

- Highly self-motivated and entrepreneurial
- Opportunity focused and results driven



- Ability to work in a cross-functional manner within a flat organization
- Clear vision, sense of urgency, effective prioritization, and adjustment of activities; Effective time management, leverage, or resources
- High level of proactive communication - written and oral - and Build networks, collects and shares wisdom through training/experience

Qualifications:

- RFIC design and leadership in analog and RF integrated circuits for wireless applications
- Supervise the entire design flow and implementation cycles to produce a competitive RFIC transceiver products
- 10 plus years of experience in RF/Analog/digital electronics application area with 5 plus years of people management experience
- This position requires a Bachelor's or Master of Science Degree in EE
- Technical depth in one or more of the following areas
 - PLL/ Freq synth background
 - PMU/PMIC design knowledge
 - Rectifier design
 - CMOS and GaN PA design and optimizations
 - SoC and IP integration
- Experience with tools such ADS, ADS momentum, Cadence Spectre RF, virtuoso Layout, HFSS etc.
- Experience with test and measurement products such as network analyzers, signal generators, digital oscilloscopes and spectrum analyzers

Location:

San Jose or Costa Mesa, CA