



Senior RF Hardware Engineer

Energoous Corporation (Nasdaq: WATT) is the Wireless Power Network global leader. Our award-winning WattUp® wireless charging solution is the only technology that supports both contact and distance charging through a fully compatible ecosystem. Built atop fast, efficient, and highly scalable RF-based charging technology. Energoous develops silicon-based wireless power transfer (WPT) technologies and customizable reference designs, and provides worldwide regulatory assistance, a reliable supply chain, quality assurance, and sales and technical support to global customers. The company received the world's first FCC Part 18 certification for at-a-distance wireless charging and has been awarded over 200 patents for its WattUp wireless charging technology to-date.

Our goal is simple: To power everything from the critical tools and devices that keep factories running to the instruments and wearables that monitor patient health – wirelessly. Our next-generation technology -- built atop innovative engineering and backed by hundreds of patents -- supports a near-limitless range of applications without the need for cumbersome charging cables and ports that limit innovation and are prone to failure. WattUp delivers advanced capabilities and design flexibility to global manufacturers who are building the latest consumer, medical, military, and industrial devices, among many other sectors.

Essential Duties:

- Design RF-focused reference boards for wireless charging components and systems
- Characterize RF Devices (Amplifiers, Rectifiers, passives) and systems
- Contribute to all phases of component and system product development: architecture definition, transistor-level circuit design, multi-chip module design, layout, lab debug, validation and production support

Qualifications:

- MSEE+ 3-5 years of experience or BSEE with 5-10 years' experience
- Experience with RF power amplifier, multi-chip module, RF front-end design and product development
- Board level PCB design experience is highly desirable
- Strong RF fundamentals such as impedance matching, active/passive RF device modeling, resonant circuits and RF/wireless communication systems
- Proficiency with circuit simulation tools (such as ADS/AWR) and electromagnetic simulation tools (such as HFSS/CST)
- Experience with RFIC/analog circuit design is desirable
- Knowledge of common wireless communication standards desirable
- Hands on experience with lab debugging and RF instruments
- Strong communication and problem-solving skills. Capable of working closely with cross-functional teams, including test engineers, product engineers, application engineers, program managers and marketing

