Energous Corporation (Nasdaq: WATT) is the Wireless Power Network global leader. Our award-winning WattUp® wireless charging solution is the only technology that supports both contact and distance charging through a fully compatible ecosystem. Built atop fast, efficient, and highly scalable RF-based charging technology. Energous develops silicon-based wireless power transfer (WPT) technologies and customizable reference designs, and provides worldwide regulatory assistance, a reliable supply chain, quality assurance, and sales and technical support to global customers. The company received the world’s first FCC Part 18 certification for at-a-distance wireless charging and has been awarded over 200 patents for its WattUp wireless charging technology to-date.

Our goal is simple: To power everything from the critical tools and devices that keep factories running to the instruments and wearables that monitor patient health – wirelessly. Our next-generation technology – built atop innovative engineering and backed by hundreds of patents – supports a near-limitless range of applications without the need for cumbersome charging cables and ports that limit innovation and are prone to failure. WattUp delivers advanced capabilities and design flexibility to global manufacturers who are building the latest consumer, medical, military, and industrial devices, among many other sectors.

**Essential Duties:**

- Manage a small team responsible for RF wireless power reference boards and systems
- Cross-functional engineering project management for key projects
- As an individual contributor, generate and own architecture, system requirements, and board design for wireless power SOC reference boards and systems
- System/Chip bringup & DVT Leader
- Manufacturing test hardware development
- Regulatory support
- Tier-1 customer support including transition to MP with top CMs and ODMs

**Skills/Qualifications:**

- MSEE with minimum of 8 years experience, including at least 3 years people management experience
- Expert in Board-Level Hardware Design
  - Architecture and schematics, component selection, DFx for high volume mass production
  - PCB layout including best practices for RF, high-speed digital, and power systems
  - System bringup and integration
- Excellent lab/test practices across multiple domains
- Circuit and System simulation experience with ADS, Matlab, Python etc.

**Location of Employment:**
San Jose, CA (Santa Clara County)