



Principal Wireless Hardware Design Engineer

Essential Duties:

As part of the Energous team you will design and test advanced wireless power transmission solutions. You will develop reference designs and solutions using novel RF architectures. You will be responsible for design definition, documentation, schematic capture, BOM and layout of low cost designs based on the WATTUP technology, working under tight schedules and time to market constraints.

Experience designing commercial Wi-Fi, Bluetooth, power management, and ARM CPU solutions is required. Must be familiar with development, bring-up, and characterization of complex wireless systems.

Qualifications:

The candidate must have at least 5 years of experience in the design and fabrication of Wireless PCB reference cards and modules in the million-unit production volume. The candidate should have experience in all design stages including design definition, documentation, schematic, layout design for cost and DFM, low cost BOM component selection, PCB bring up and characterization over PVT.

The candidate must show expertise in the design of WiFi or Bluetooth solutions, but experience in other relevant wireless technologies may be considered. He/she must have a strong RF and digital design background. It is a plus if the candidate shows knowledge of ARM based designs, FPGAs, etc. Candidate must show expertise using PCB CAD tools including Orcad, Altium, Mentor Graphics, PADS, HFSS, etc. Candidate must show proficiency in using lab equipment including Network Analyzer, Signal generator Oscilloscope, etc. He/she must show good written and oral skills.